

inputting data symbols to a logic unit over a plurality of successive symbol periods, wherein each data symbol comprises a plurality of information bits:

forming in the logic unit at least two derived bits during each symbol period by combining selected information bits;

forming, during each symbol period, a plurality of bit sequences, each bit sequence containing bits input or derived over a plurality of symbol periods;

generating, during each symbol period, a plurality of filtered waveform segments using the bit sequences;

combining, during each symbol period, at least two of the filtered waveform segments to obtain a segment of the imaginary waveform part;

combining, during each symbol period, at least two of the filtered waveform segments to obtain a segment of the real waveform part.

Remarks

Amendments have been made to correct minor errors in the specification and to claim

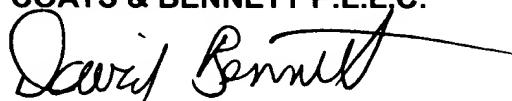
21. No new matter has been added.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "Version with markings to show changes made."

Respectfully submitted,

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